UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/989,761	11/19/2001	Kenneth Y. Ogami	CYPR-CD01179M	2006

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EXAMINER
DO, THUAN V

ART UNIT	PAPER NUMBER
2825	

DATE MAILED: 10/03/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 10/03)

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE _3_ MONTH(S) FROM
THE MAILING DATE OF THIS COMMUNICATION.
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed
  after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any
  earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☑ Responsive to communication(s) filed on 8/22/03

2a)☒ This action is **FINAL**.   2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is
closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) _1-28_ is/are pending in the application.

    4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) _1-28_ is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11)☐ The proposed drawing correction filed on _____ is: a)☐ approved b)☐ disapproved by the Examiner.

    If approved, corrected drawings are required in reply to this Office action.

12)☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a)☐ All b)☐ Some * c)☐ None of:

      1.☐ Certified copies of the priority documents have been received.

      2.☐ Certified copies of the priority documents have been received in Application No. _____ .

      3.☐ Copies of the certified copies of the priority documents have been received in this National Stage
application from the International Bureau (PCT Rule 17.2(a)).

    * See the attached detailed Office action for a list of the certified copies not received.

14)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

    a) ☐ The translation of the foreign language provisional application has been received.

15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)

2) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ .

4) ☐ Interview Summary (PTO-413) Paper No(s). _____ .

5) ☐ Notice of Informal Patent Application (PTO-152)

6) ☐ Other: .

DETAILED ACTION

1.   This final office action is responsive to the preliminary amendment entered on
09/03/2003. Claims 1-28 are pending in this office action.

Claim objection

Claim 1, the term "...the selecting the... "; and

Claims 1,3,4,7,12,13,17,23, the word "...possible... " are unclear to what
applicant intend to mean. Delete the word "possible" and/or correction the other term
are required.

The new search found the prior art of Duboc and the rejection is introduced as
following:

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that
form the basis for the rejections under this section made in this Office action:

> (e) the invention was described in-
> (1) an application for patent, published under section 122(b), by another filed in the United States
> before the invention by the applicant for patent, except that an international application filed under the
> treaty defined in section 351(a) shall have the effect under this subsection of a national application
> published under section 122(b) only if the international application designating the United States was
> published under Article 21(2)(a) of such treaty in the English language; or
> (2) a patent granted on an application for patent by another filed in the United States before the
> invention by the applicant for patent, except that a patent shall not be deemed filed in the United
> States for the purposes of this subsection based on the filing of an international application filed under
> the treaty defined in section 351(a).

2.   Claims 1-28 are rejected under 35 U.S.C. 102(e) as being unpatentable over
Duboc et al., Pat. No. 6,587,955.

Regarding claim 1: Duboc teaches a method comprising:

selecting the global electronic resource via an input device wherein the global
electronic resource is associated with a first electronic design project (col. 10, lines 51-
67 where parameters are selected as electronic resource) ;

displaying a plurality of possible parameter values for the global electronic resource in response to the selecting the global electronic resource (col. 11, lines 65-67);

choosing one of the plurality of possible parameter values as a chosen parameter value via the input device (col. 11, line 65 through col. 12, line 3); and

storing the chosen parameter value as a default global setting for use by second electronic design project (col. 6, lines 49-67 where the chosen parameter value contained in the program module as the electronic design automation in col. 8, lines 32-45).

Regarding claim 2: Duboc teaches a method with second electronic design project (Figure 5, at least the returned loop to the input of box 86 for the second design function).

Regarding claim 3: Duboc teaches a method with displaying pop-up list (col. 5, lines 6-18).

Regarding claim 4: Duboc teaches a method with parameter values (col. 3, lines 31-49).

Regarding claim 5: Duboc teaches a method with input device (col. 5, lines 45-52).

Regarding claims 6: Duboc teaches a method with chosen parmeter value (col. 3, lines 31-49).

Regarding claims 7,17: These claims teach the method and program similar to the method of claim 1 and rejected in the similar manner.

Regarding claims 8,9,10: Duboc teaches a method with input device (col. 5, lines 45-52).

Regarding claim 11: Duboc teaches a method with pop-up list (col. 5, lines 6-18).

Regarding claim 12: Duboc teaches a system for the same method of claim 1 and rejected in the same rationale except tracking a location function is taught in the column 14, lines 17-29 with tracking cycle function.

Regarding claim 13: Duboc teaches a method with global parmeters (col. 5, lines 45-52).

Regarding claims 14,15,16: Duboc teaches a method with input device (col. 5, lines 45-52).

Regarding claims 18,19: Duboc teaches a method with selecting (col. 10, lines 51-67).

Regarding claim 20: Duboc teaches a method with updating a memory (col. 10, lines 4-14) and propagating global electronic design (col. 5, lines 45-52).

Regarding claim 21: Duboc teaches a method with microcontroller circuit (Figure 1).

Regarding claim 22: Duboc teaches a method with pop-up list (col. 5, lines 6-18).

Regarding claim 23: Duboc teaches a system comprising:

a processor coupled to a bus (Figure 1); and

a memory coupled to said, said memory containing instructions for implementing a method of processing global design parameters, said method comprising:

a) displaying, in tabular form, a list of global design parameters and respective global design parameter values associated therewith for use in a first design project (col. 11, lines 65-67);

b) in response to a user selection of a selected global design parameter, displaying a window comprising a plurality of possible values for said selected global design parameter (col. 10, lines 51-67 where parameters are selected as electronic resource);

c) in response to a user selection of a selected value of said possible values, assigning said selected global design parameter to said selected value (col. 11, line 65 through col. 12, line 3); and

d) in response to the user selection of the selected value of said possible values, storing said selected global design parameter to a default global setting for use in a second design project (col. 6, lines 49-67 where the chosen parameter value

contained in the program module as the electronic design automation in col. 8, lines 32-45).

Regarding claim 24: Duboc teaches a system with selecting (col. 10, lines 51-67).

Regarding claim 25: Duboc teaches a system with cursor device (col. 5, lines 45-52).

Regarding claim 26: Duboc teaches a system with updating a memory (col. 10, lines 4-14) and propagating global electronic design (col. 5, lines 45-52).

Regarding claim 27: Duboc teaches a system with microcontroller circuit (Figure 1).

Regarding claim 28: Duboc teaches a system with pop-up list (col. 5, lines 6-18).

Response to Arguments

3.   Applicant's arguments with respect to claims 1-28 have been considered but are moot in view of the new ground(s) of rejection.

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL.** See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

CONTACT INFORMATION

4.   Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuan Do whose telephone number is 703-305-2362. The examiner can normally be reached on Monday-Friday 8:30-5:30 (except 2nd Fridays).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on 703-308-1323. The fax phone numbers for the organization where this application or proceeding is assigned are 703305-3431 for regular communications and 703-305-3431 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0596.

Thuan Do
Patent examiner
9/17/03

LEIGH M. GARBOWSKI
PRIMARY EXAMINER